

isc Silicon NPN Power Transistor
2SC1141
DESCRIPTION

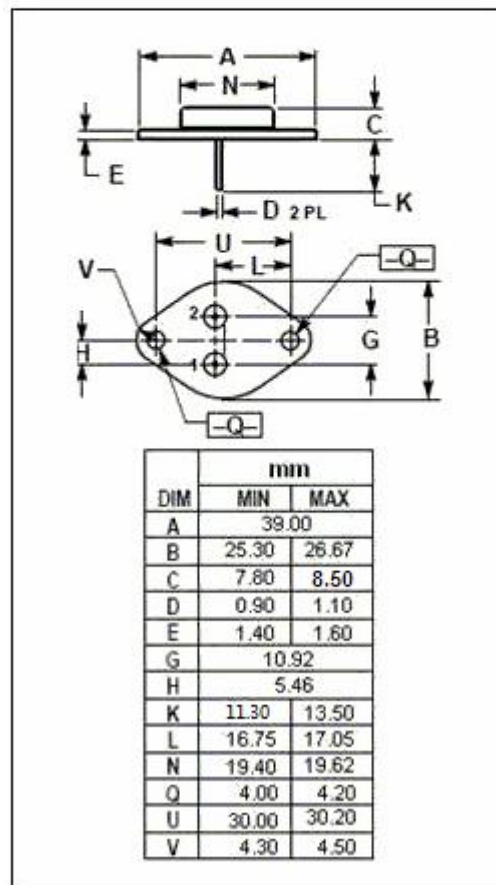
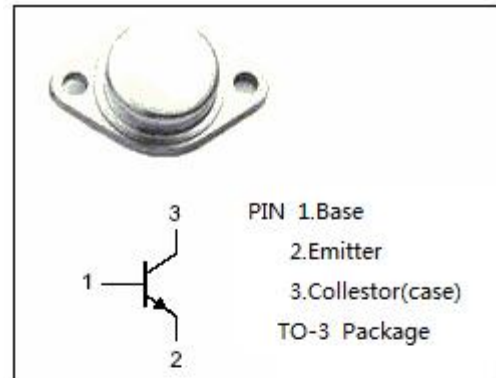
- With TO-3 Package
- High voltage
- Wide area of safe operation
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- For power amplifier applications

ABSOLUTE MAXIMUM RATINGS(T_a=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	600	V
V _{CEO}	Collector-Emitter Voltage	300	V
V _{EBO}	Emitter-Base Voltage	7	V
I _C	Collector Current-Continuous	15	A
P _C	Collector Power Dissipation	150	W
T _J	Junction Temperature	200	°C
T _{stg}	Storage Temperature Range	-65~200	°C



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ELECTRICAL CHARACTERISTICS
 $T_c=25^{\circ}\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C=8\text{A}; I_B=1.6\text{A}$			1.5	V
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	$I_C=8\text{A}; I_B=1.6\text{A}$			1.6	V
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage	$I_C=25\text{mA}; I_B=0$	300			V
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage	$I_E=1\text{mA}; I_C=0$	7			V
h_{FE-1}	DC Current Gain	$I_C=1.0\text{A}; V_{CE}=5\text{V}$	15		50	
h_{FE-2}	DC Current Gain	$I_C=7.5\text{A}; V_{CE}=5\text{V}$	10		25	
f_T	Current-Gain—Bandwidth Product	$I_C=1.0\text{A}; V_{CE}=12\text{V}; f_{test}=1\text{MHz}$	10			MHz

NOTICE:

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